

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	17	703/27.ccls. and @pd>="20090501"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2009/11/06 16:26
L2	22	((tim\$4 or performance) with annotat\$5) same (code or assembl\$2 or line)) and simulat\$4 and @pd>="20090501"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2009/11/06 16:30

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	1	(annotat\$4 and (software adj simulation)).clm.	US-PGPUB	OR	ON	2009/11/06 16:48
L4	56	(annotat\$4 and (simulation)).clm.	US-PGPUB	OR	ON	2009/11/06 16:50
L5	9	(annotat\$4 and timing and (simulation)).clm.	US-PGPUB	OR	ON	2009/11/06 16:55
L6	9	(annotat\$4 and timing and (performance)).clm.	US-PGPUB	OR	ON	2009/11/06 16:57

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Results 1 - 10 of about **16,200**. (0.15 sec)

Worst-case execution-time analysis at low cost- ▶ [psu.edu](#) [PDF]

P Puschner - Control Engineering Practice, 1998 - Elsevier

... A low-level WCET analysis tool then computes detailed **timing** information of the **annotated assembler** programs, and passes the information back to the source ...

Cited by 22 - Related articles - BL Direct - All 8 versions

A compilation-based software estimation scheme for hardware/software co-simulation-

▶ [psu.edu](#) [PDF]

M Lajolo, M Lazarescu, A Sangiovanni- ... - Proceedings of the seventh international workshop on ... 1999 - portal.acm.org

... The **key idea** is to use the GNU-C compiler (CCC) to generate "**assembler-level**" C code. This code can be **annotated** with **timing** information, and used as a ...

Cited by 55 - Related articles - All 13 versions

Compilation-based software performance estimation for system level design

M Lazarescu, JR Bammi, E Harcourt, L ... - Proc. of the 6th Intl. Workshop on Hardware/Software ... 1998 - doi.ieeecomputersociety.org

... compiler. The output **assembler** is translated to an **assembler-level** C co-simulation model, **annotated** with **timing** information. The ...

Cited by 23 - Related articles - All 3 versions

A sequence assembly and editing program for efficient management of large projects-

▶ [nih.gov](#) [PDF]

S Dear, R Staden - Nucleic acids research, 1991 - Oxford Univ Press

... We describe a sequence **assembly** and editing program ... and has substantially reduced the **time** taken to ... editor also provides facilities for **annotating** the sequences ...

Cited by 257 - Related articles - All 7 versions

[PDF] ▶ Incorporating worst-case execution time in a commercial C-compiler

H Börjesson - Master's thesis, Department of Computer Systems, ... 1995 - Citeseer

... 29 8 Analytic **Timing** Tools 30 8.1 ... 30 8.3 \The **Annotation Approach** : : : 31 8.4 ...

Cited by 13 - Related articles - View as HTML - All 4 versions

Annotated calendar assembly

JP Vincent, ME Weidmann - US Patent 4,645,238, 1987 - Google Patents

... with the prior more specifically to **assembly** of an **annotated** calendar w^{rk} of others is that the positional relationship to **time** containing abbreviated event ...

Cited by 14 - Related articles - All 2 versions

[PDF] ▶ A review of worst-case execution-time analyses

P Puschner, A Burns - REAL TIME SYSTEMS-AVENEI NJ., 2000 - vmars.tuwien.ac.at

... Mok and his group (Mok et al., 1989 Amerasinghe, 1985 Chen, 1987) produced a **timing** tool for **assembly** language programs, which are **annotated** by TAL (**timing** ...

Cited by 91 - Related articles - View as HTML - BL Direct - All 2 versions

Whole-genome random sequencing and assembly of Haemophilus influenzae Rd

RD Fleischmann, MD Adams, O White, RA ... - Science, 1995 - sciencemag.org
 ... An approach for genome analysis based on sequencing and **assembly** of unselected ... sequenced with automated technology (5). At the present **time**, there are active ge ...
 Cited by 3608 - Related articles - BL Direct - All 12 versions

[PDF] ► **Allowing cycle-stealing direct memory access I/O concurrent with hard-real-time programs**

TY Huang, JWS Liu, JY Chung - Int. Conf. on Parallel and Distributed Systems, 1996 - Citeseer
 ... Mok et al. 5] implemented a **timing** tool that nds the worst-case execution paths of **assembly** language programs and bounds their WCETs. ...
 Cited by 7 - Related articles - View as HTML - All 4 versions

Code size minimization and retargetable **assembly** for custom EPIC and VLIW instruction ...-

► **psu.edu** [PDF]

S Aditya, SA Mahike, BR Rau - ACM Transactions on Design Automation of ..., 2000 - portal.acm.org
 ... of these measures are applied by PICO-VLIW at the **time** that the ... approach, the compiler needs to profile the program and **annotate** the **assembly** code with ...
 Cited by 20 - Related articles - BL Direct - All 11 versions

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annotate assembler timing

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Scholar - Results 1 - 10 of about **14,900**. (0.13 sec)

Using the SimOS machine simulator to study complex computer systems- [psu.edu](#) [PDF]
M Rosenblum, E Bugnion, S Devine, SA ... - Transactions on Modeling and Computer Simulation ..., 1997 -
portal.acm.org

... memory-system stall, and inter-processor communication **time**... and can control the
classification of **simulation** statistics ... For example, an **annotation** put on the ...

Cited by 298 - Related articles - BL Direct - All 17 versions

Gradient-based optimization of custom circuits using a static-timing formulation- [uci.edu](#)

[PDF]

AR Conn, IM Elfadel, WW Molzen Jr, PR O' ... - Proceedings of the 36th ACM/IEEE conference on ..., 1999 -
portal.acm.org

... Tuning under control of the optimizer 1. Final **timing** 2. Summarize results 3.

Back-**annotate Simulation** & gradient calculation via API Netlist ...

Cited by 71 - Related articles - BL Direct - All 10 versions

An **annotated** bibliography of interactive program steering- [psu.edu](#) [PDF]

W Gu, J Vetter, K Schwan - ACM Sigplan Notices, 1994 - portal.acm.org

... Software developers must **annotate** the existing code, however. ... steering code to produce
a **time-accurate**, unsteady finite-element **simulation** in [33]. ...

Cited by 76 - Related articles - BL Direct - All 15 versions

... and apparatus for back-**annotating timing** constraints into **simulation** models of field

PF Dombrowski - US Patent 5,581,738, 1996 - Google Patents

US Patent 5,581,738 Dec. 3, 1996 Sheet 1 of 23 O ... US Patent Dec. 3, 1996

Sheet 2 of 23 5,581,738 -52 50 FIG. 2 ... US Patent 5,581,738 Dec. 3, 1996 Sheet

3 of 23 84*) 68 FIG.3a 96 94 92 FIG.3b 62 ... US Patent 5,581,738 Dec. ...

Cited by 21 - Related articles - All 2 versions

Integrating path and **timing** analysis using instruction-level **simulation** techniques- [psu.edu](#)

[PDF]

T Lundqvist, P Stenström - Lecture Notes in Computer Science, 1998 - Springer

... potential to perform arbitrarily detailed **timing** analysis of ... Second, by extending

the **simulation** technique with the ... without the need for **annotating** the programs ...

Cited by 75 - Related articles - BL Direct - All 13 versions

An integrated path and **timing** analysis method based on cycle-level symbolic execution-

[psu.edu](#) [PDF]

T Lundqvist, P Stenström - Real-Time Systems, 1999 - Springer

... AN INTEGRATED PATH AND **TIMING** ANALYSIS METHOD ... since the branch condition is determined
during **simulation**. Also, no loop bound **annotation** for the inner loop is ...

Cited by 133 - Related articles - All 9 versions

... of electronic design from behavioral description, including generation of logic and **timing** ...

MD Rostoker, C Dangelo, OS Bair - US Patent 5,572,437, 1996 - Google Patents

... 211-217. "Chip **Simulation** Is All a Matter of Image", by Thacker et al., BSD: The

Electronic System Design Magazine, Nov. 1988, pp. 65-70. "Area-Time Model for ...

Cited by 85 - Related articles - All 4 versions

Optimizing Communication in embedded system cosimulation- ► [psu.edu](#) (PDF)

K Hines, G Borriello - Proc. International Workshop on Hardware/Software ..., 1997 - doi.ieee.computersociety.org

... each of the optimized versions of any driver routine take the same amount of **simulation**

time. Transaction **timing** can be included in an **annotation** as follows: ...

Cited by 35 - Related articles - All 17 versions

MOSSIM: A switch-level simulator for MOS LSI

RE Bryant - Proceedings of the 18th conference on Design ..., 1981 - portal.acm.org

... a further simplification, a unit delay **timing** model is ... can be read from an **annotated**

check plot ... Before beginning the **simulation**, the network is partitioned and ...

Cited by 81 - Related articles - All 5 versions

... level, behavior-oriented description, including interactive schematic design and **simulation**

MD Rostoker, C Dangelo, DR Watkins - US Patent 5,544,067, 1996 - Google Patents

... VHDL COMPILER SYNTHESIS OPTIMIZATION STATIC **TIMING** ANALYSIS VHDL ... SYSTEM -

FLOORPLANNING

-BACK **ANNOTATION** VHDL COMPILER & **SIMULATOR** SILICON INDEPENDENT i...

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Scholar Preferences

Scholar 1980 - 2000 [include citations ▼]

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Method and apparatus for back-annotating timing constraints into simulation models of ...

PF Dombrowski - US Patent 5,581,738, 1996 - Google Patents
US Patent 5,581,738 Dec. 3, 1996 Sheet 1 of 23 O ... US Patent Dec. 3, 1996
Sheet 2 of 23 5,581,738 -52 50 FIG. 2 ... US Patent 5,581,738 Dec. 3, 1996 Sheet
3 of 23 84*) 68 FIG. 3a 96 94 92 FIG. 3b 62 ... US Patent 5,581,738 Dec. ...

Cited by 21 - Related articles - All 2 versions

... design from behavioral description, including generation of logic and timing models

MD Rostoker, C Dangelo, OS Bair - US Patent 5,572,437, 1996 - Google Patents
... logic-level models incorporating accurate timing (and delay ... AUTOMODELING i r 404
406 SIMULATION PATTERNS SPICE SUB-CIRCUITS MULTI-MODE MODEL MULTI-MODE ...

Cited by 85 - Related articles - All 4 versions

An integrated path and timing analysis method based on cycle-level symbolic execution-

► psu.edu [PDF]
T Lundqvist, P Stenström - Real-Time Systems, 1999 - Springer

... Also, no loop bound annotation for the inner loop ... to update the WCET properly during
simulation, the simulator must of course be extended to model the timing ...

Cited by 133 - Related articles - All 9 versions

Integrating path and timing analysis using instruction-level simulation techniques- ► psu.edu

[PDF]
T Lundqvist, P Stenström - Lecture Notes in Computer Science, 1998 - Springer
... iterations, without the need for annotating the programs ... to update the WCET properly
during simulation, the simulator ... of course be extended to model the timing ...

Cited by 75 - Related articles - BL Direct - All 13 versions

Using the SimOS machine simulator to study complex computer systems- ► psu.edu [PDF]

M Rosenblum, E Bugnion, S Devine, SA ... Transactions on Modeling and Computer Simulation ..., 1997 -
portal.acm.org

... it is executing, the hardware simulation models of SimOS ... can control the classification
of simulation statistics. For example, an annotation put on the context ...

Cited by 298 - Related articles - BL Direct - All 17 versions

Gradient-based optimization of custom circuits using a static-timing formulation- ► uci.edu

[PDF]
AR Conn, IM Eifadel, WW Molzen Jr, PR O' ... - Proceedings of the 36th ACM/IEEE conference on 1999 -
portal.acm.org

... the results of convex optimization using approximate delay models. ... Final timing 2.
Summarize results 3. Back-annotate Simulation & gradient calculation via API ...

Cited by 71 - Related articles - BL Direct - All 10 versions

Timing constraint specification and synthesis in behavioral VHDL- ► york.ac.uk [PDF]

P Eles, K Kuchinski, Z Peng, A Doboli - Proceedings of the conference on European design ..., 1995 -
portal.acm.org

... above and the correspondence between the timing behavior of the constrained sequences

in the synthesized hardware and in the back-annotated simulation model. ...

Cited by 16 - Related articles - All 9 versions

A compilation-based software estimation scheme for hardware/software co-simulation-

► [psu.edu](#) [PDF]

M Lajolo, M Lazarescu, A Sangiovanni- ... - Proceedings of the seventh international workshop on ..., 1999 - portal.acm.org

... to guess the compiler optimizations and annotating the original ... is precise, but suffers from a low simulation speed, and requires a detailed model of the ...

Cited by 55 - Related articles - All 13 versions

Symbolic simulation—techniques and applications- ► [psu.edu](#) [PDF]

RE Bryant - Proceedings of the 27th ACM/IEEE conference on ..., 1991 - portal.acm.org

... power and in the size of circuit they can model. ... uninitialized state variable i in the simulation, they assign ... they view them as unknown, but annotated values. ...

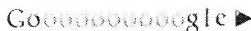
Cited by 64 - Related articles - All 7 versions

Fast hardware/software co-simulation for virtual prototyping and trade-off analysis

C Passerone, L Lavagno, M Chiodo, A ... - Proceedings of the 34th annual conference on Design ..., 1997 - portal.acm.org

... very same code that will be run on the target processor, together with timing annotation obtained by performance estimation, as simulation model for software ...

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annotate simulation model timing

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